

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	AUTOMATIC BIT FAIL MAPPING FOR EMBEDDED MEMORIES WITH CLOCK MULTIPLIERS	
Application Number :		
Date :		
First Named Applicant:	Darren L. Anand	
Confirmation Number:		
Attorney Docket Number:	BUR920030168US1	
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Documents being submitted:

us-ids

us-assignment

us-request

us-fee-sheet

us-declaration

us-declaration

application-body

Files

BUR920030168US1-usidst.xml

us-ids.dtd

us-ids.xsl

BUR920030168US1-usassn.xml

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Comments